Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

## TIME-TABLE

# M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER, 2015

**TIME: 10:00 AM TO 1:00 PM** 

Branch: CIVIL				
Date & Day	STRUCTURAL ENGINEERING	TRANSPORTATION ENGINEERING	GEOTECHNICAL ENGINEERING	
15-09-2015 (Tuesday)	COMPUTER ORIENTED NUMERICAL METHODS (30M06)	TRAFFIC ENGINEERING AND MANAGEMENT (31201)	ADVANCED SOIL MECHANICS (31301)	
18-09-2015 (Friday)	THEORY OF ELASTICITY AND PLASTICITY (31101)	TRANSPORT INFRASTRUCTURE DESIGN (31202)	ADVANCED FOUNDATION ENGINEERING (31302)	
21-09-2015 (Monday)	THEORY AND ANALYSIS OF PLATES (31102)	PAVEMENT ANALYSIS AND DESIGN (31204)	SOIL DYNAMICS AND MACHINE FOUNDATIONS (31303)	
23-09-2015 (Wednesday)	ADVANCED STRUCTURAL ANALYSIS (311B1)	URBAN TRANSPORTATION POLICY AND PLANNING FOR SUSTAINABALE DEVELOPMENT	ENGINEERING OF GROUND (31304)	
26-09-2015 (Saturday)		GROUND IMPROVEMENT METHODS (312A2)	GEO- ENVIRONMENTAL ENGINEERING (313A2)	
29-09-2015 (Tuesday)		CONCRETE TECHNOLOGY (312B2)	GROUNDWATER CONTAMINATION AND REMEDIATION (313B2)	

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 18-08-2015

Copy to:

Chief Controller of Examinations

- 1) HOD (CE) is requested to inform the students and circulation among staff for information and necessary
- 2) Controller of Examinations for information & necessary action.
- 3) To be displayed in all Notice Boards.
- 4) PA to Principal for filing

Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

## TIME-TABLE

# M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER, 2015

**TIME: 10:00 AM TO 1:00 PM** 

Branch: EEE				
Date & Day	Control Engineering	CONTROL SYSTEMS	POWER ELECTRONICS AND ELECTRICAL DIRIVES	
15-09-2015 (Tuesday)	ADVANCED CONTROL SYSTEMS (32101)	·	1	
18-09-2015 (Friday)	DIGITAL CONTROL SYSTEMS (32102)	DIGITAL CONTROL SYSTEMS (32102)	DIGITAL CONTROL SYSTEMS (32102)	
21-09-2015 (Monday)	STATE AND PARAMETER ESTIMATION THEORY (32103)	STATE AND PARAMETER ESTIMATION THEORY (32103)		
23-09-2015 (Wednesday)	DSP PROCESSOR ARCHITECTURE AND APPLICATIONS (34113)	DSP PROCESSOR ARCHITECTURE AND APPLICATIONS (34113)	POWER ELECTRONIC CONTROL OF DC DRIVES (32304)	
O 26-09-2015 (Saturday)	TRANSDUCERS AND DATA ACQUISITION SYSTEMS (321A2)			
29-09-2015 (Tuesday)	MICROCONTROLLERS AND APPLICATIONS (34114)	MICROCONTROLLERS AND APPLICATIONS (34114)		

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 18-08-2015

Copy to:

Chief Controller of Examinations

- 1) HOD (EEE) is requested to inform the students and circulation among staff for information and necessary action
- 2) Controller of Examinations for information & necessary action.
- 3) To be displayed in all Notice Boards.
- 4) Website I/c to display in college website.
- 5) DA to Principal for filing

Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

#### TIME-TABLE

## M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER,2015

TIME: 10:00 AM TO 1:00 PM

Branch: ME				
Date & Day	THERMAL ENGINEERING	ADVANED MANUFACTURING SYSTEMS		
15-09-2015 (Tuesday)	ADVANCED OPTIMIZATION TECHNIQUES & APPLICATIONS (33101)	AUTOMATION IN MANFACTURING (33201)		
18-09-2015 (Friday)	ADVANCED THRMODYNAMICS (33102)	PRECISION ENGINEERING (33203)		
21-09-2015 (Monday)	ADVANCED HEAT & MASS TRANSFER (33103)	DESIGN FOR MANUFACTURING & ASSEMBLY (33204)		
23-09-2015 (Wednesday)	ADVANCED FLUID MECHANICS (33104)	SPECIAL MANUFACTURING PROCESS (332A1)		
26-09-2015 (Saturday)	SOLAR ENERGY TECHNOLOGY (331A3)			
29-09-2015 (Tuesday)	ADVANCED I.C ENGINES (331B1)			

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 18-08-2015

Chief Controller of Examinations

Conv to:

- 1) HOD (ME) is requested to inform the students and circulation among staff for information and necessary action
- 2) Controller of Examinations for information & necessary action.
- 3) To be displayed in all Notice Boards.
- 4) Website I/c to display in college website.
- 5) PA to Principal for filing



Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

#### TIME-TABLE

## M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER,2015

**TIME: 10:00 AM TO 1:00 PM** 

Branch: ECE				
Date & Day	DS&CE	VLSI SYSTEM DESIGN		
15-09-2015 (Tuesday)	VLSI TECHNOLOGY AND DESIGN (34101)	VLSI TECHNOLOGY AND DESIGN (34101)		
18-09-2015 (Friday)	ADVANCED DATA COMMUNICATIONS (34103)			
21-09-2015 (Monday)	DIGITAL SYSTEM DESIGN (34102)	DIGITAL SYSTEM DESIGN (34102)		
23-09-2015 (Wednesday)	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN (34104)	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN (34104)		
26-09-2015 (Saturday)		CPLD & FPGA ARCHITECTURES AND APPLICATIONS (341C1)		
29-09-2015 (Tuesday)	CMOS DIGITAL INTEGRATED CIRCUIT DESIGN (341B1)	CMOS DIGITAL INTEGRATED CIRCUIT DESIGN (341B1)		

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 18-08-2015

Chief Controller of Examinations

#### Copy to:

- 1) HOD (ECE) is requested to inform the students and circulation among staff for information and necessary action
- 2) Controller of Examinations for information & necessary action.
- 3) To be displayed in all Notice Boards.
- 4) Website I/c to display in college website.
- 5) PA to Principal for filing



Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

#### TIME-TABLE

## M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER,2015

TIME: 10:00 AM TO 1:00 PM

Branch: CSE		
Date & Day	COMPUTER SCIENCE & ENGINEERING	COMPUTER SCIENCE
15-09-2015 (Tuesday)	ADVANCED DATA STRUCTURES AND ALGORITHMS (35101)	ADVANCED DATA STRUCTURES AND ALGORITHMS (35101)
18-09-2015 (Friday)	COMUTER SYSTEM DESIGN (35102)	COMUTER SYSTEM DESIGN (35102)
21-09-2015 (Monday)	ADVANCED OPERATING SYSTEMS (35103)	ADVANCED OPERATING SYSTEMS (35103)
23-09-2015 (Wednesday)	DISTRIBUTED SYSTEMS (35104)	<b></b>
26-09-2015 (Saturday)	SOFTWARE PROCESS AND PROJECT MANAGEMENT (351B3)	SOFTWARE PROCESS AND PROJECT MANAGEMENT (351B3)
29-09-2015 (Tuesday)	SOFTWARE ARCHITECTURE AND DESIGN PATTERNS (351B3)	

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 18-08-2015

Chief Controller of Examinations

- Copy to: 1) HOD (CSE) is requested to inform the students and circulation among staff for information and necessary action
- 2) Controller of Examinations for information & necessary action.
- 3) To be displayed in all Notice Boards.
- 4) Website I/c to display in college website.
- 5) PA to Principal for filing